

WEST Search History

DATE: Wednesday, December 15, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L23	119 near5 pin	8
<input type="checkbox"/>	L22	119.ti,ab.	58
<input type="checkbox"/>	L19	isolat\$ near4 clock	1000
<input type="checkbox"/>	L18	117 and l3	42
<input type="checkbox"/>	L17	112 or 113 or 114 or 115 or 116	3477
<input type="checkbox"/>	L16	713/401	756
<input type="checkbox"/>	L15	713/400	1537
<input type="checkbox"/>	L14	713/600	1231
<input type="checkbox"/>	L13	713/501	1075
<input type="checkbox"/>	L12	(713/500)[CCLS]	575
<input type="checkbox"/>	L11	(713/500)![CCLS]	575
<input type="checkbox"/>	L10	double near2 defined near3 pin	5
<input type="checkbox"/>	L9	double nesr2 defined near3 pin	0
<input type="checkbox"/>	L8	L3 near5 (mux or switch or multiplexer)	35
<input type="checkbox"/>	L7	L3 near10 clock	27
<input type="checkbox"/>	L6	L3 near10 pin	9
<input type="checkbox"/>	L5	L3 near10 clock pin	0
<input type="checkbox"/>	L4	L3 near5 chipset	4
<input type="checkbox"/>	L3	L2 or l1	1783
<input type="checkbox"/>	L2	(various or different) adj type near5 (cpu or processor)	1345
<input type="checkbox"/>	L1	(first or second) adj type near5 (cpu or processor)	502

END OF SEARCH HISTORY